



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Shivnandan D. Kaushik; Ping Sager § Group Art Unit: Unknown
Serial No.: 10/625,442 §
Filed: July 23, 2003 §
For: Determining Target Operating Frequencies For A Multiprocessor System § Examiner: Unknown
§ Atty. Dkt. No.: ITL.0970US (P16133) §

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants submit the references listed on the attached form PTO 1449, copies of the references are enclosed.

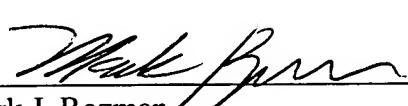
This statement is being filed before the receipt of a first Office action on the merits.
Please apply any charges or credits to Deposit Account 20-1504.

Respectfully submitted,

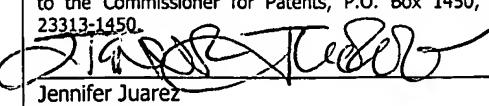
Date: January 27, 2004



21906
PATENT TRADEMARK OFFICE


Mark J. Rozman
Registration No. 42,117
TROP, PRUNER & HU, P.C.
8554 Katy Freeway, Suite 100
Houston, Texas 77024
(512) 418-9944 [Phone]
(713) 468-8883 [Fax]

Date of Deposit: January 27, 2004
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Jennifer Juarez

O I P E INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)		ATTY DOCKET NO. ITL.0970US (P16133)	SERIAL NO. 10/625,442
		APPLICANT(S): SHIVNANDAN D. KAUSHIK; PING SAGER	
		FILING DATE: July 23, 2003	GROUP ART UNIT: Unknown

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
A.						
B.						
C.						
D.						
E.						
F.						
G.						
H.						
I.						

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
						YES NO
J.						
K.						
L.						
M.						
N.						
O.						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

P.	Marr, Deborah T., et al. "Hyper-Threading Technology Architecture and Microarchitecture". Intel Technology Journal, vol. 6, no. 1, February 2002, pp. 4-15.
Q.	"Mobile Intel® Pentium® III Processors - Intel SpeedStep® Technology" Intel Corporation. http://www.intel.com/support/processors/mobile/pentiumiii/ss.htm . March 24, 2003.
R.	"Windows Platform Design Notes-Designing Hardware for the Microsoft® Windows® Family of Operating Systems-Windows Native Processor Performance Control". Microsoft® Corporation. November 12, 2003.
S.	
T.	
U.	
V.	

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.